Preface

This workshop is the tenth in a series of international workshops. This year it takes place in Göttingen, Germany, and is organized by the University of Hannover.

Göttingen has one the most famous German universities, where very well known scientists like Lichtenberg, Hilbert, Gauss and von Neumann studied, worked and taught. It also hosts several research institutes of the Max-Planck-Society. The first electronic tube calculator G1 was built in Göttingen in 1952 by H. Billing. Additionally, Göttingen was selected because it is adjacent to the world exposition EXPO 2000 in Hannover which gives an outlook into the 21st century covering the major topics of humankind, nature and technology.

With respect to these inspiring surroundings the technical program of PAT-MOS 2000 includes 10 sessions dedicated to most important subjects of power and timing modeling, optimization and simulation at the dawn of the 21st century.

The four invited talks address the European research activities in the workshop fields, the evolving needs for minimal power consumption in the area of wireless and chipcard applications and design methodologies of very highly integrated multimedia processors.

The workshop is a result of the joint work of a large number of individuals, who cannot all be mentioned here. In particular, we would like to acknowledge the outstanding work of the reviewers, who did a competent job in a timely manner. We also have to thank the members of the local organizing committee for their effort in enabling the conference to run smoothly. Finally, we gratefully acknowledge the support of all organizations and institutions sponsoring the conference.

September 2000

Peter Pirsch
Erich Barke
Dimitrios Soudris

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